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(71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]**
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **DESMICHT, Eric** [FR/FR]; Société Civile SPID c/o, 156 Boulevard Haussmann, F-75008 Paris (FR). **MUTZ, Stéphane** [FR/FR]; Société Civile SPID c/o, 156, boulevard Haussmann, F-75008 Paris (FR). **TISON, Christophe** [FR/FR]; Société Civile SPID c/o, 156, boulevard Haussmann, F-75008 Paris (FR).

(74) Agent: **CHAFFRAIX, Jean**; Société Civile SPID, 156 Boulevard Haussmann, F-75008 Paris (FR).

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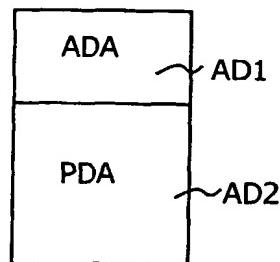
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(54) Title: CHIP INTEGRATED PROTECTION MEANS.

NVM



(57) Abstract: The invention relates to a chip for processing a content, comprising at least a microprocessor. Said chip includes an integrated non-volatile programmable memory for storing protection data and protected data, said protection data being intended to be used for authorizing/denying access to said protected data by said microprocessor under execution of a program. The invention allows to protect program and data dedicated to a chip-integrated conditional-access system and to protect features as external connections and downloaded data directly on the chip.

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